



Docket No.: 386998041US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Erik S. Jeng

Application No.: 10/731,517

Confirmation No.: 3730

Filed: December 9, 2003

Art Unit: 2815

For: NONVOLATILE MEMORY WITH SPACER
TRAPPING STRUCTURE

Examiner: E. Lee

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage of a PCT application, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Office Action or Notice of Allowance (37 CFR 1.97(c)).

Applicant submits herewith copies of non-patent literature in accordance with 37 CFR 1.98(a)(2).

This Information Disclosure Statement is not to be construed as a representation that: (i) a search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the cited information is, or is considered to be, material to patentability. In addition, applicant does not admit that any enclosed item of information constitutes prior art to the subject invention and specifically reserves the right to demonstrate that any such reference is not prior art.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

Our check in the amount of \$180.00 covering the fee set forth in 37 CFR 1.17(p) is enclosed. The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0665, under Order No. 386998041US.

Dated: November 28, 2006

Respectfully submitted,

By 

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PERKINS COIE LLP

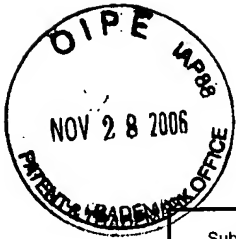
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PTO/SB/08a/b (07-06)

Approved for use through 09/30/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Complete if Known		
			Application Number	10/731,517-Conf. #3730	
			Filing Date	December 9, 2003	
			First Named Inventor	Erik S. Jeng	
			Art Unit	2815	
			Examiner Name	E. Lee	
Sheet	1	of	1	Attorney Docket Number	386998041US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
	CA	Jeng, Erik S. et al, "Investigation of Programming Charge Distribution in Nonoverlapped Implantation nMOSFETs", IEEE Trans. Electron Devices, Vol. 53, no. 10, pp. 2517-2524, Oct. 2006).		
	CB	Luksy, E. et al, "Investigation of channel hot electron injection by localized charge-trapping nonvolatile memory devices," IEEE Trans. Electron Devices, vol. 51, no. 3, pp. 444-451, Mar. 2004).		
	CC	Makwana, Jitu J. et al, "A Nonvolatile Memory Overview". (website: http://aplawrence.com/Makwana/nonvolmem.html).		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Examiner Signature	Date Considered
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